IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

Seiji Funaba, et al.

Examiner:

Benjamin P. Sandvik

Serial No.:

10/672,551

Art Unit:

2826

Filed:

September 26, 2003

Docket:

17072

Dated:

July 2, 2008

For:

SEMICONDUCTOR UNIT

FOR EVERY ONE INPUT/OUTPUT SIGNAL

HAVING TWO DEVICE TERMINALS

Confirmation No.: 3724

Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

SUBMISSION UNDER 37 C.F.R. §1.114

Sir:

This paper is filed concurrently with a Request for Continued Examination under 37 C.F.R. § 1.114 and in fulfillment of the submission requirement therein, responsive to the Office Action mailed January 2, 2008, in the above-referenced application. Favorable reconsideration and withdrawal of all rejections is kindly requested in light of the following amendments and accompanying remarks. Please amend the application as follows.

CERTIFICATION OF ELECTRONIC FILING

I hereby certify that this correspondence is being deposited with the United States Patent and Trademark Office via Electronic Filing through the United States Patent and Trademark Office e-business website, on July 2, 2008.

Dated: July 2, 2008

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